

FIG. 1 (PRIOR ART)

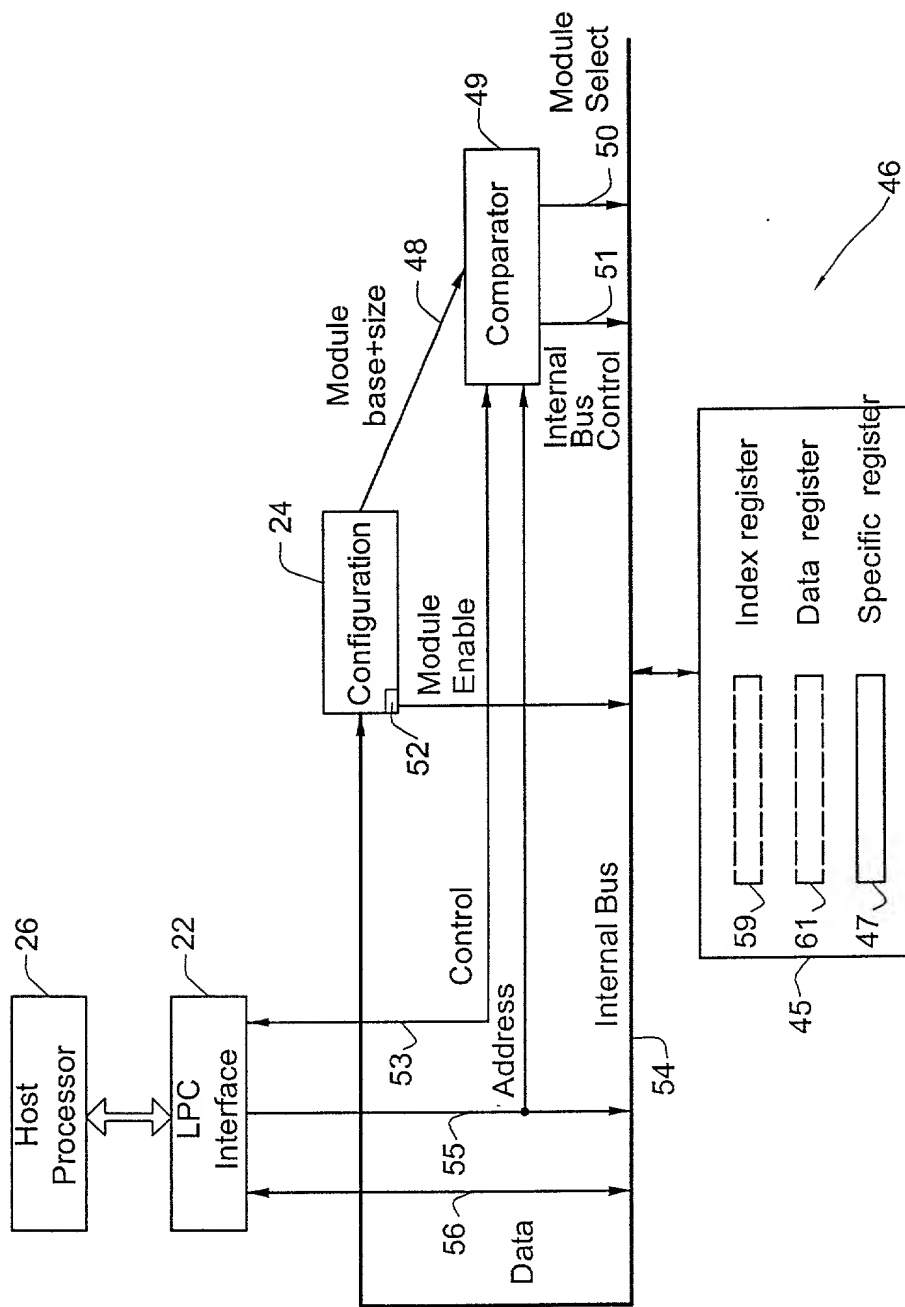


FIG. 2 (PRIOR ART)

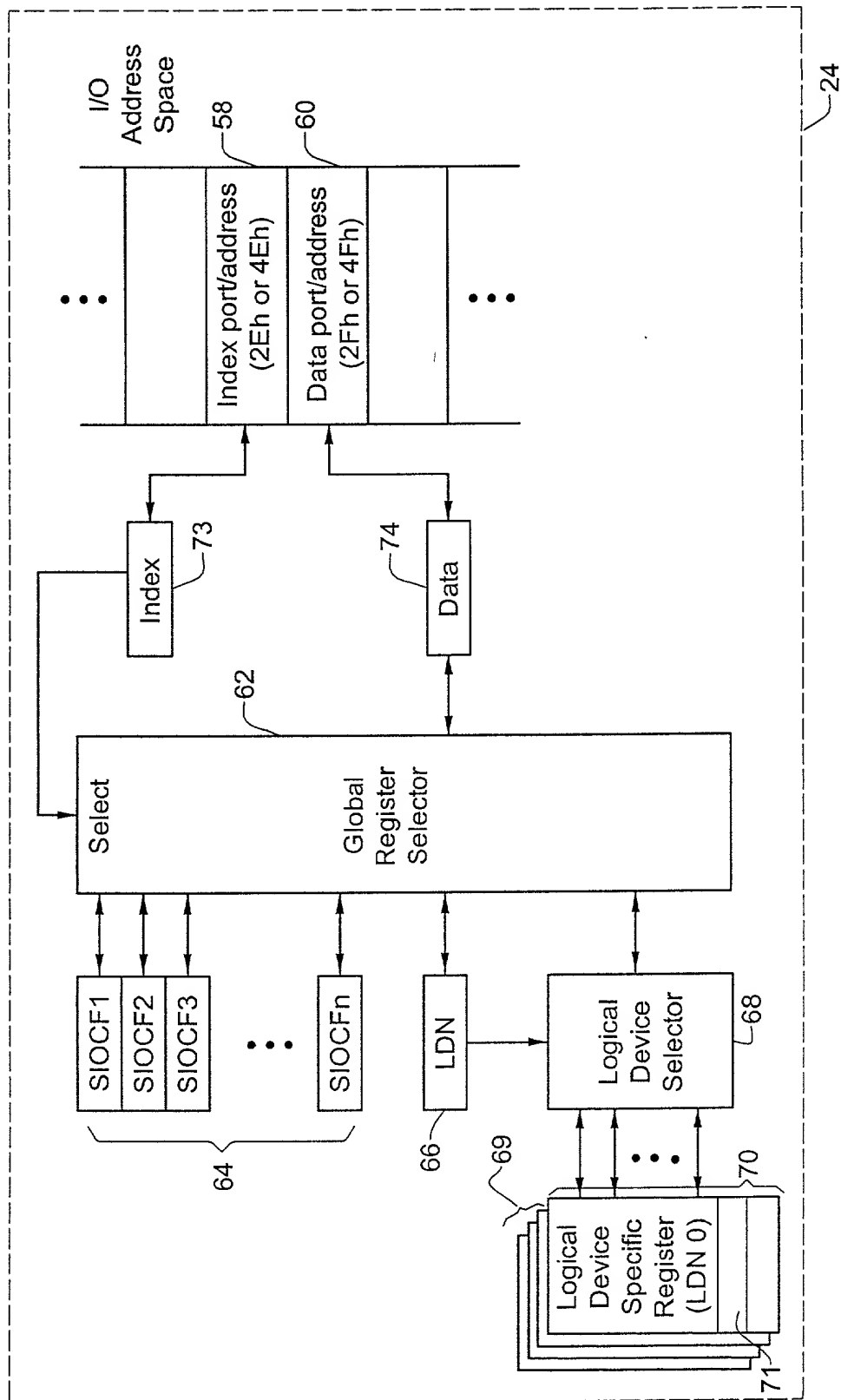
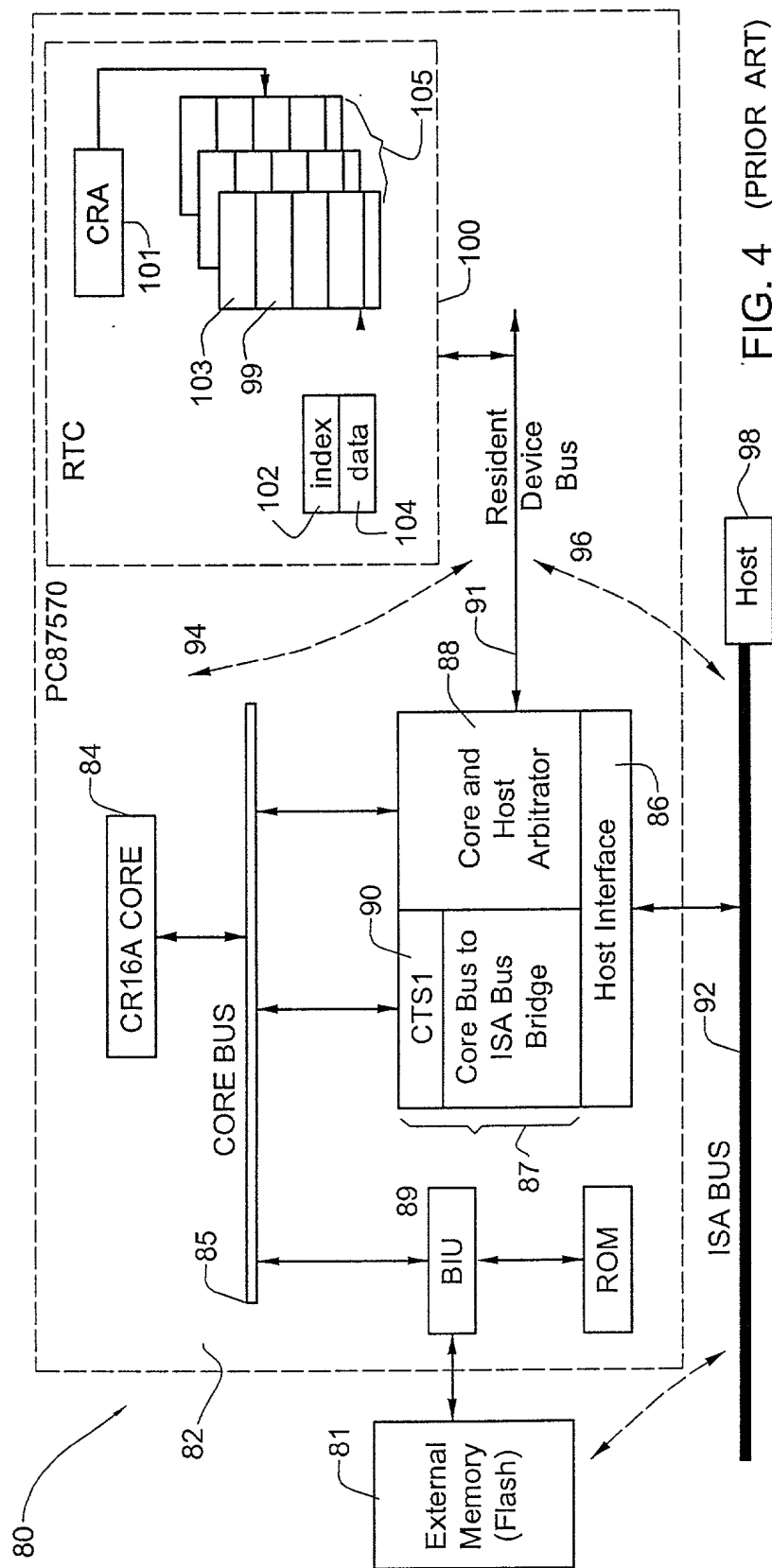


FIG. 3 (PRIOR ART)



CTS1	7	6	5	4	3	2	1	0
	Res			HMRA	HPWRON	RTCLV	RTCMR	LKRTCHA

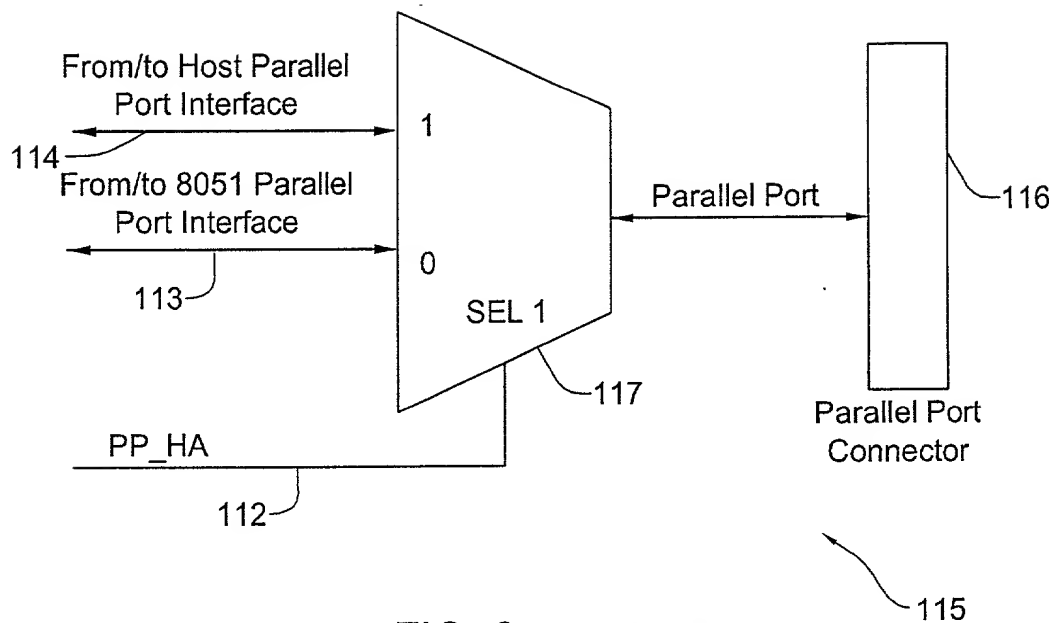


FIG. 6 (PRIOR ART)

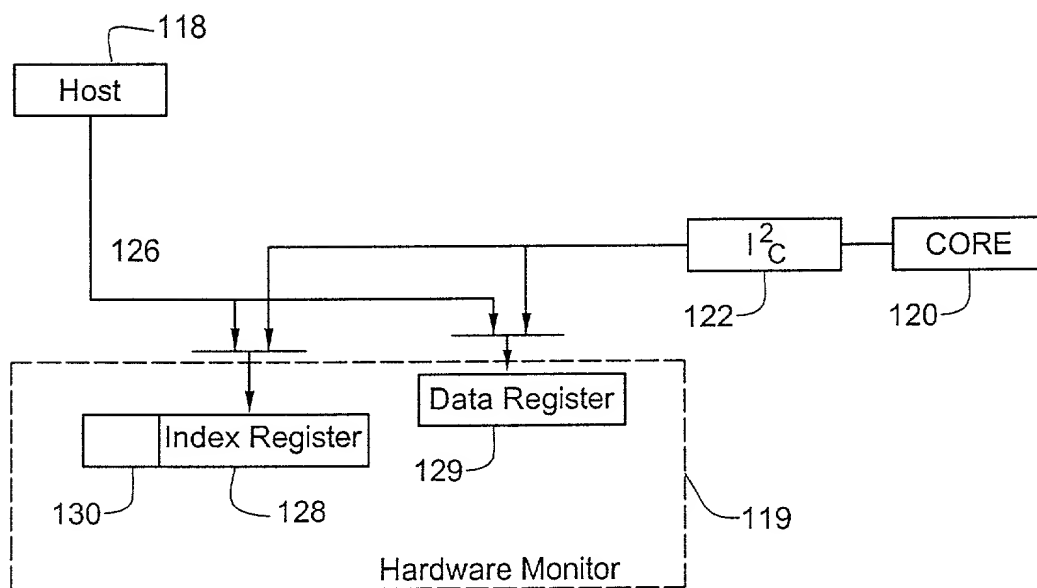


FIG. 7 (PRIOR ART)

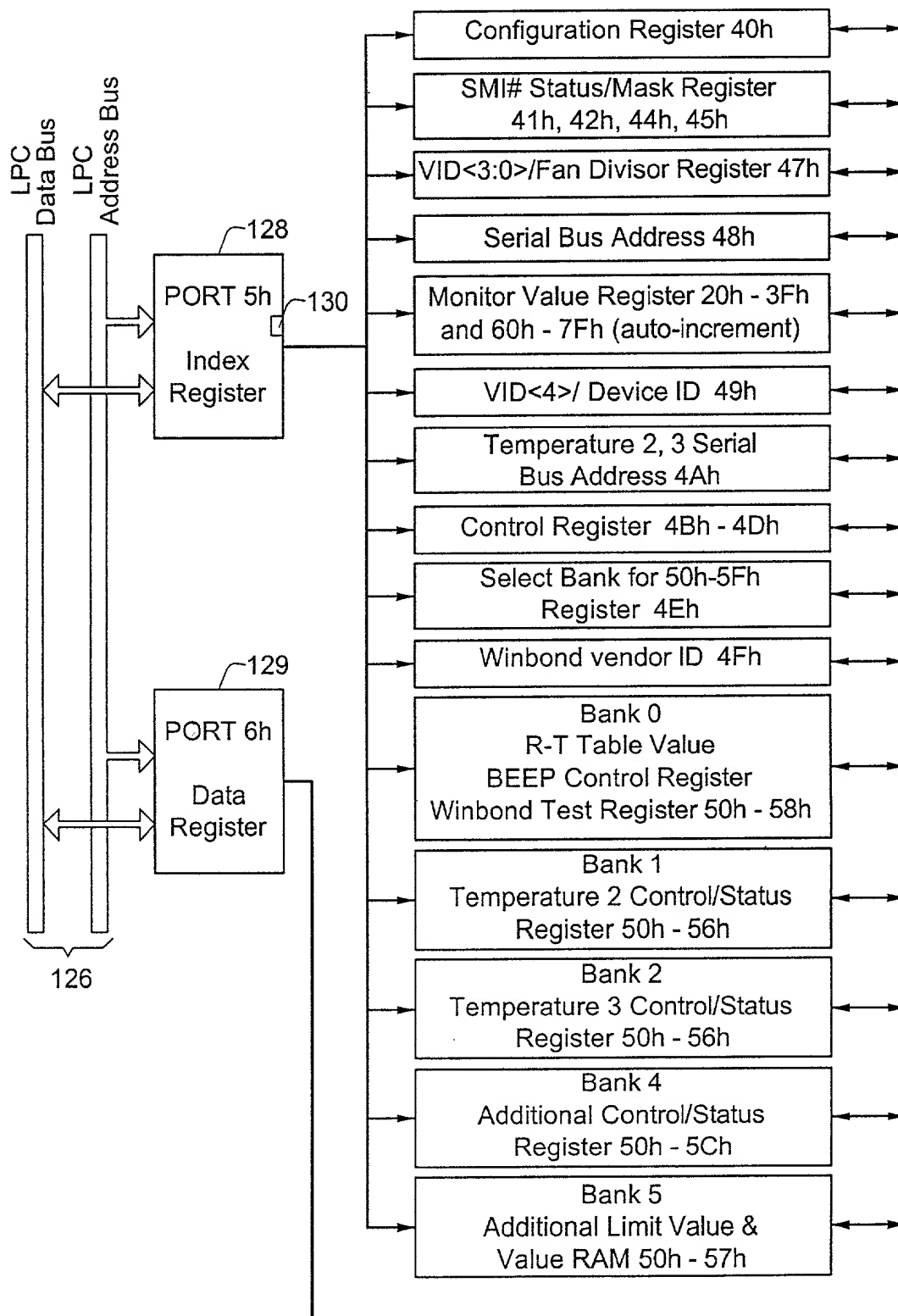


FIG. 8 (PRIOR ART)

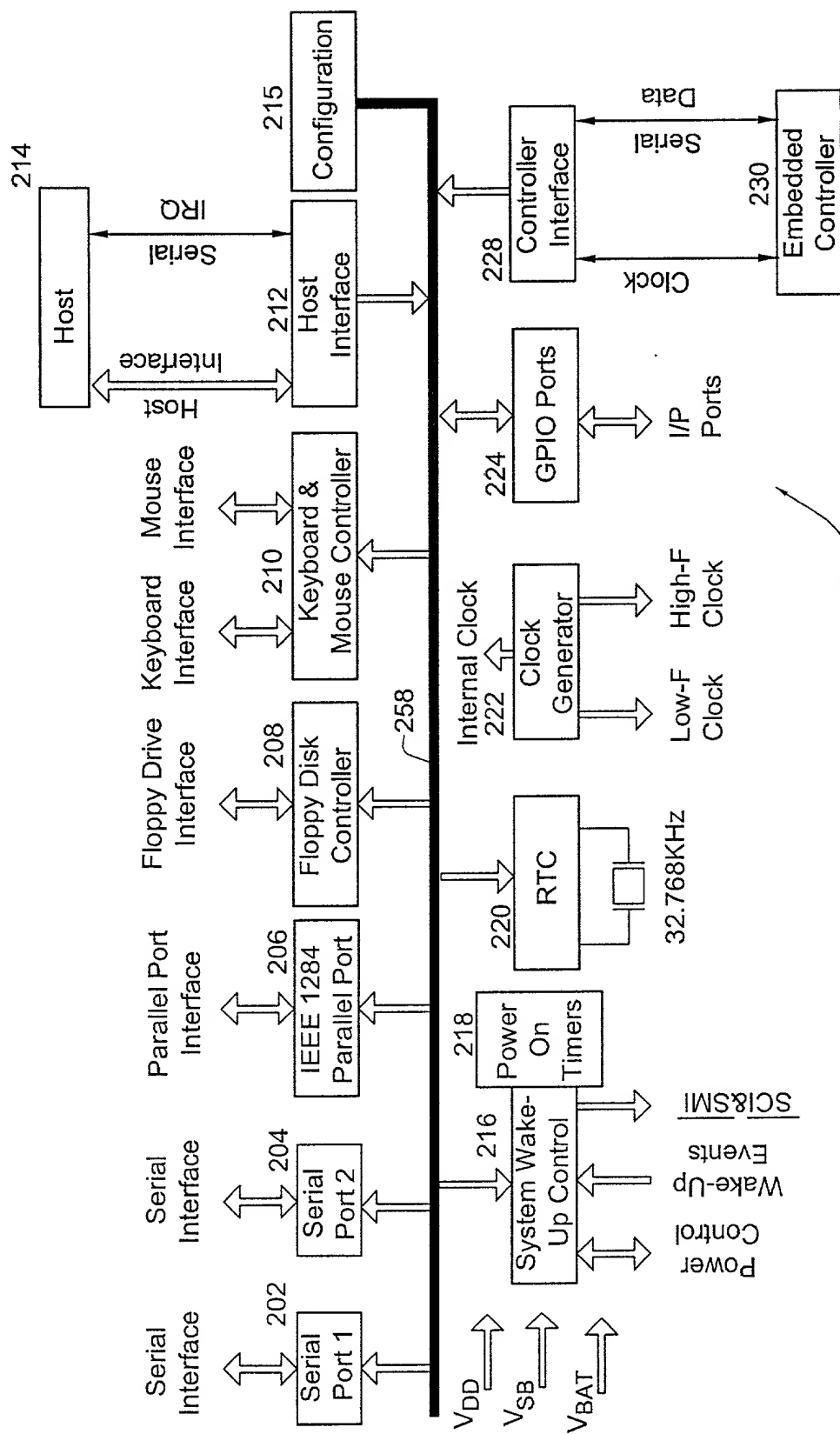


FIG. 9 198

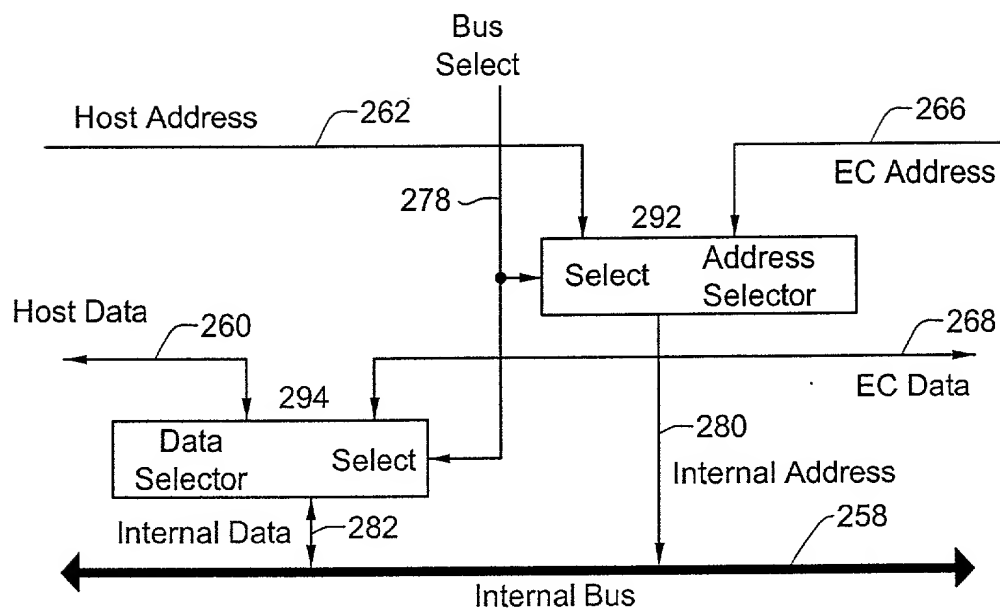


FIG. 11

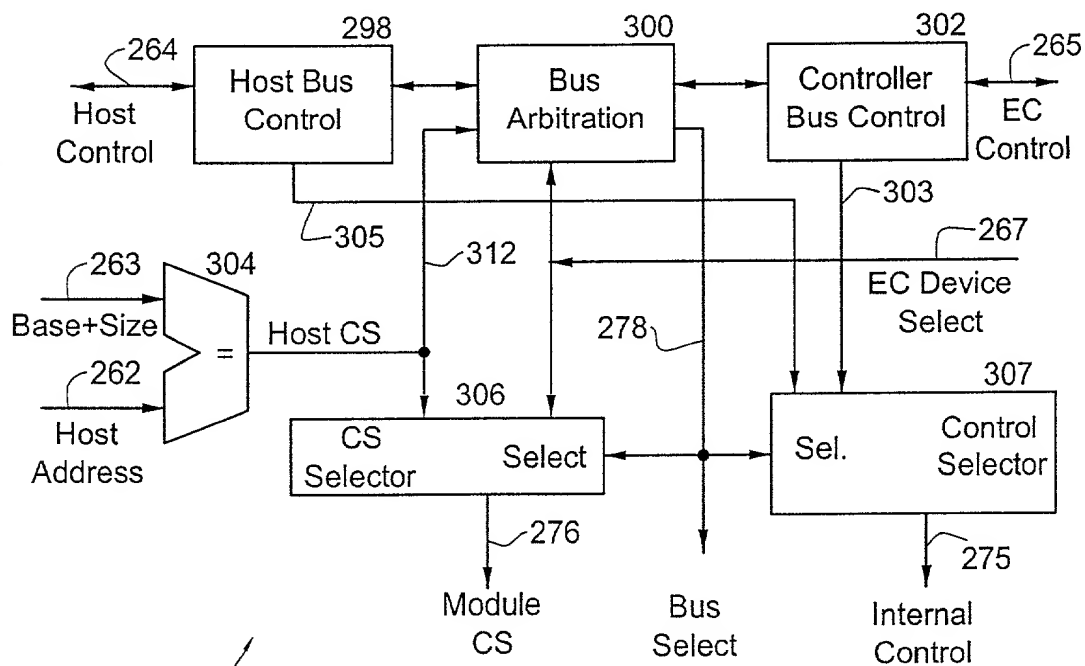
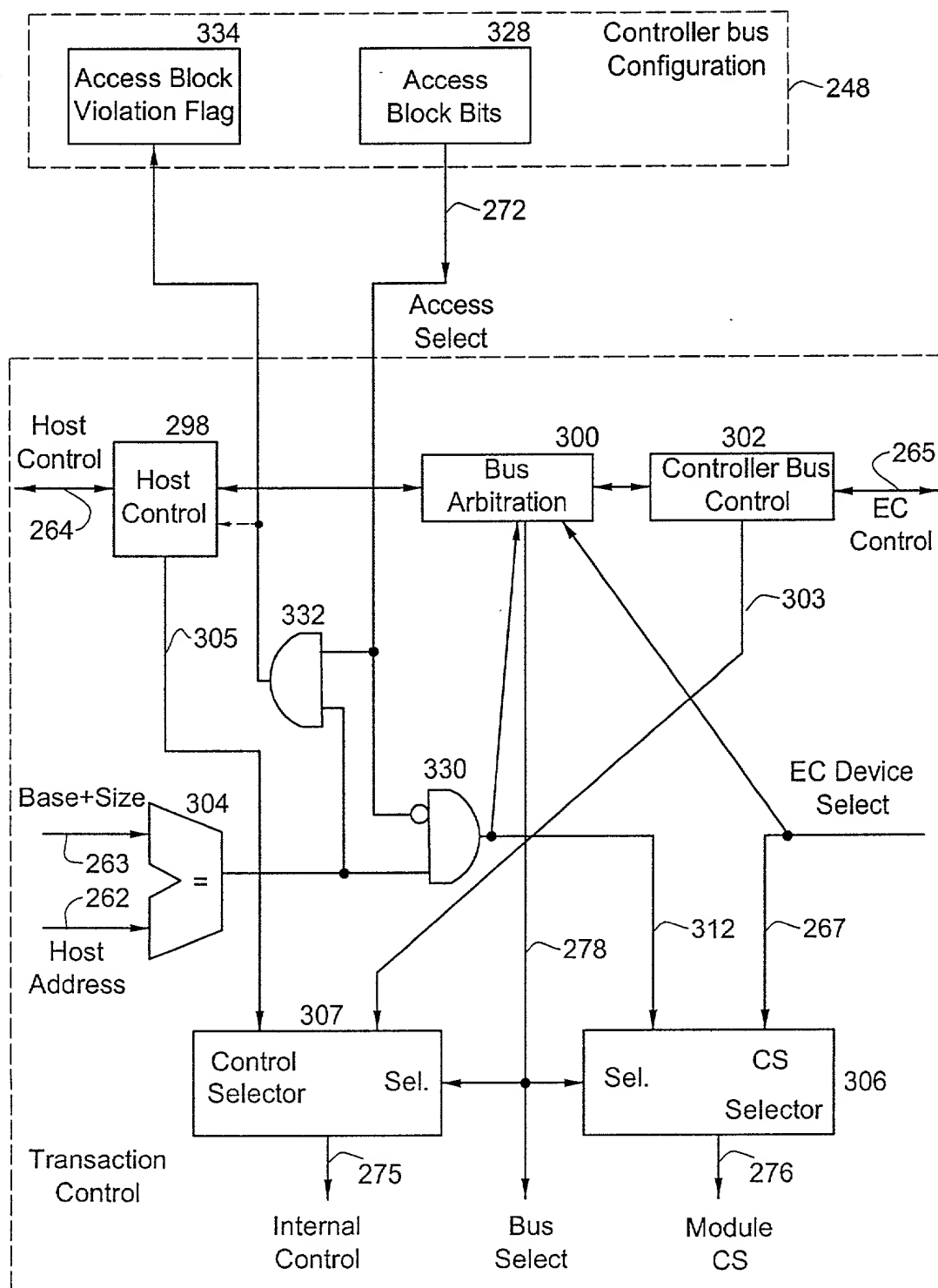
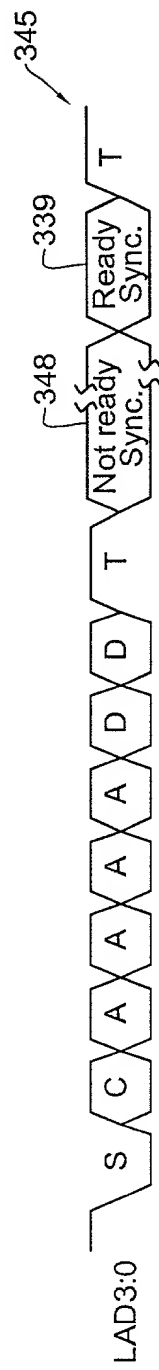
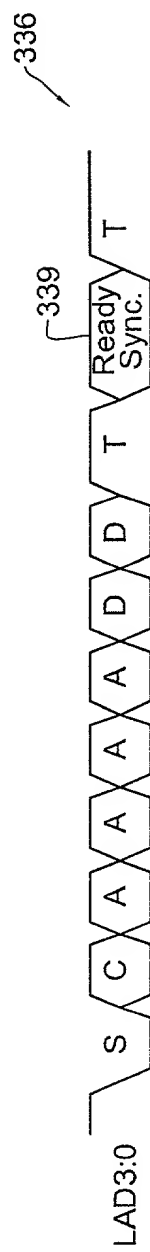
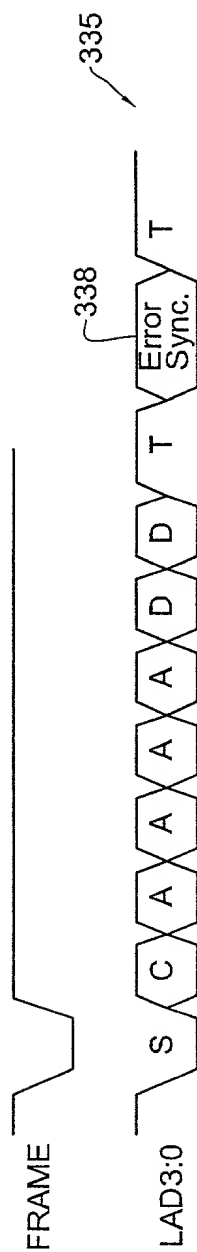


FIG. 12

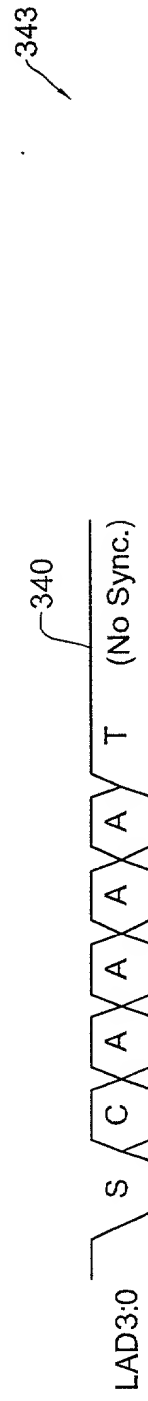
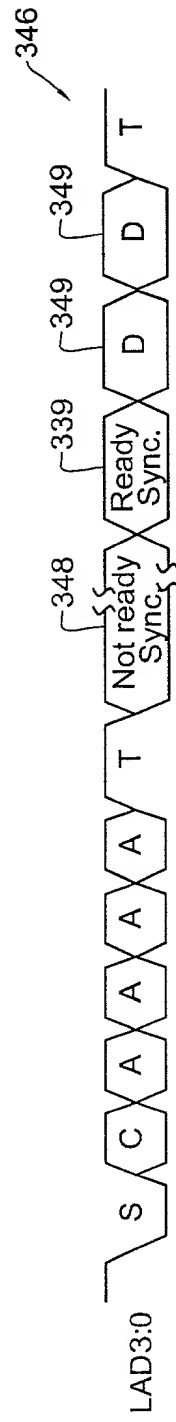
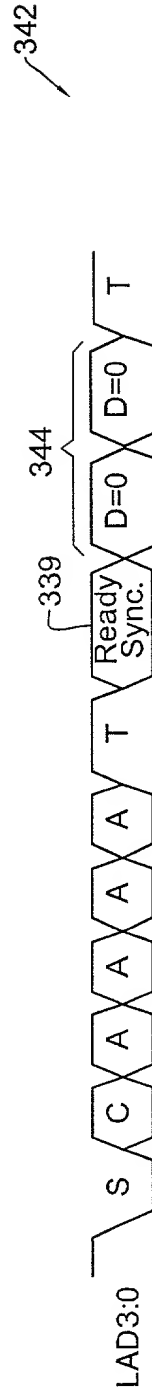
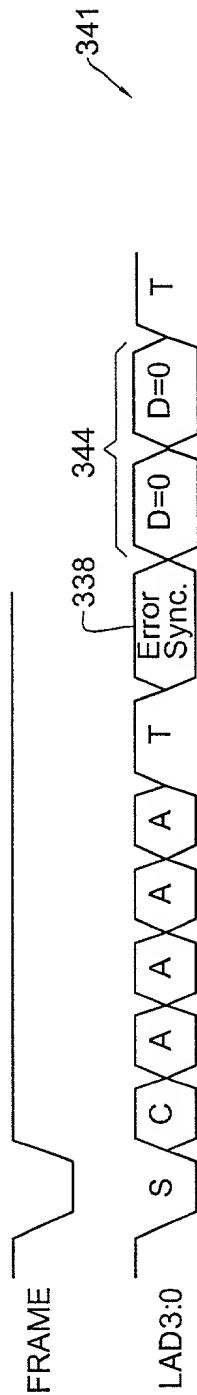


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FIG. 13



S = Start
C = Cycle Type
T = TAR
A = Address
D = Data



S = Start
C = Cycle Type
T = TAR
A = Address
D = Data

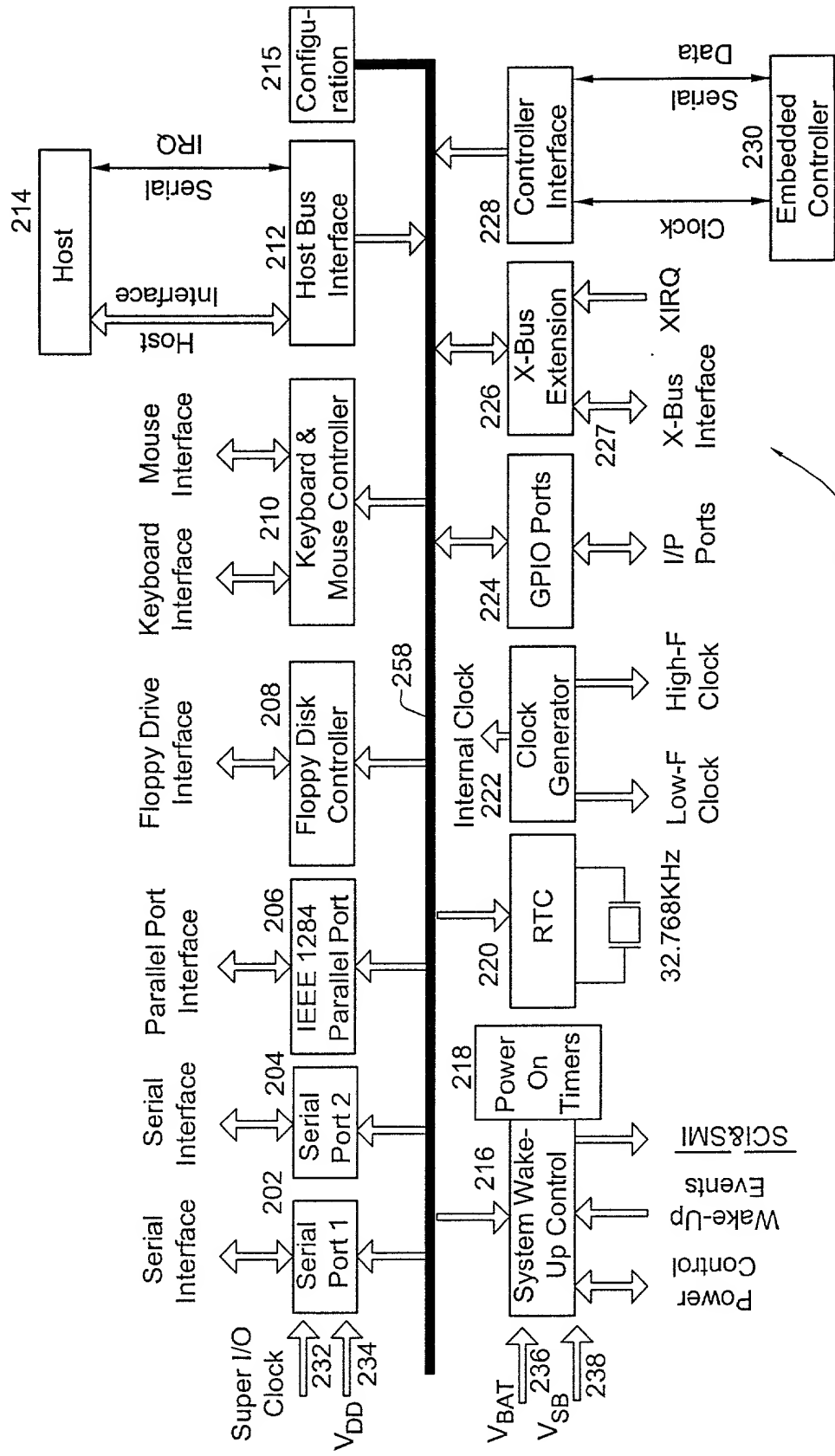


FIG. 16

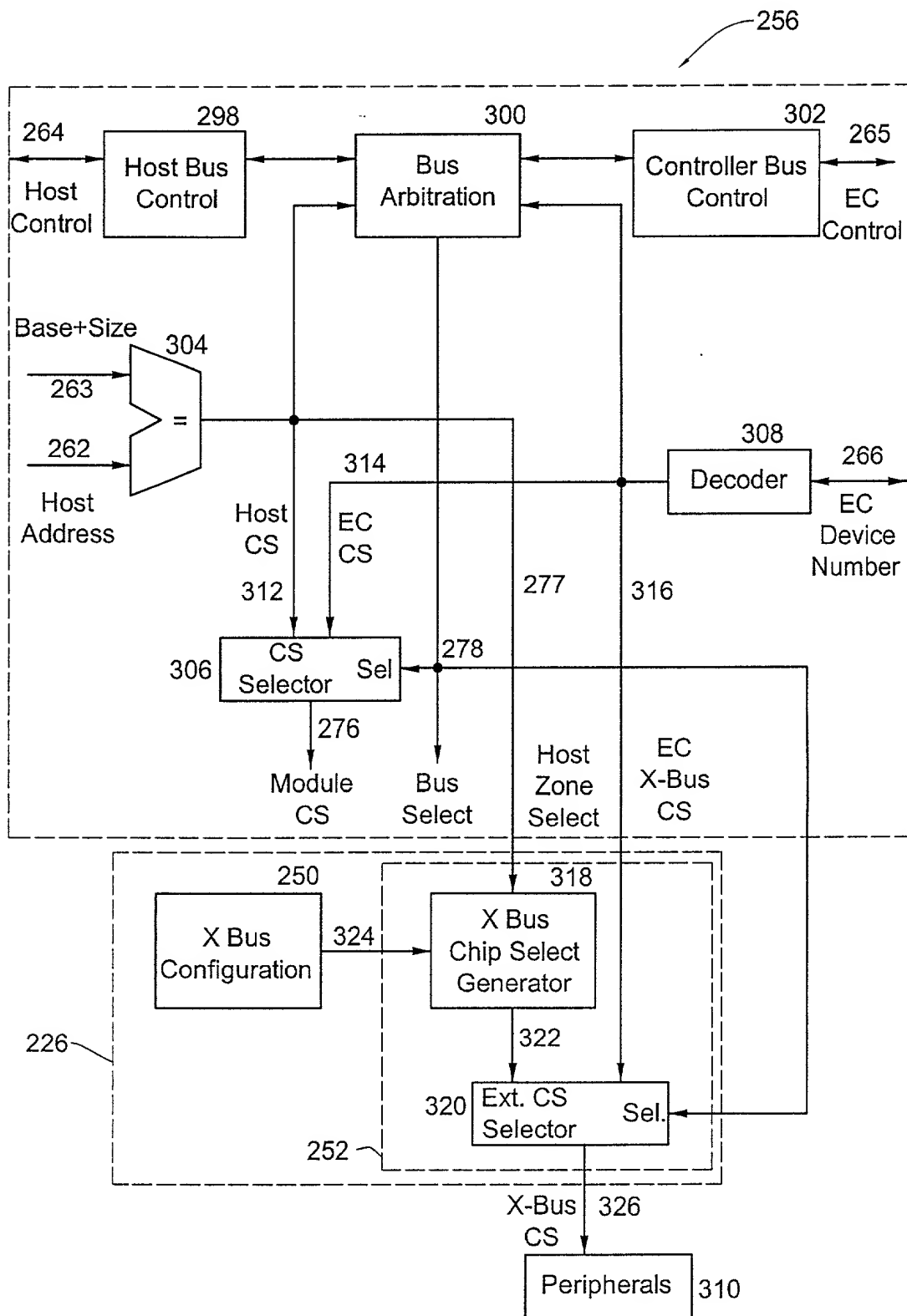


FIG. 17

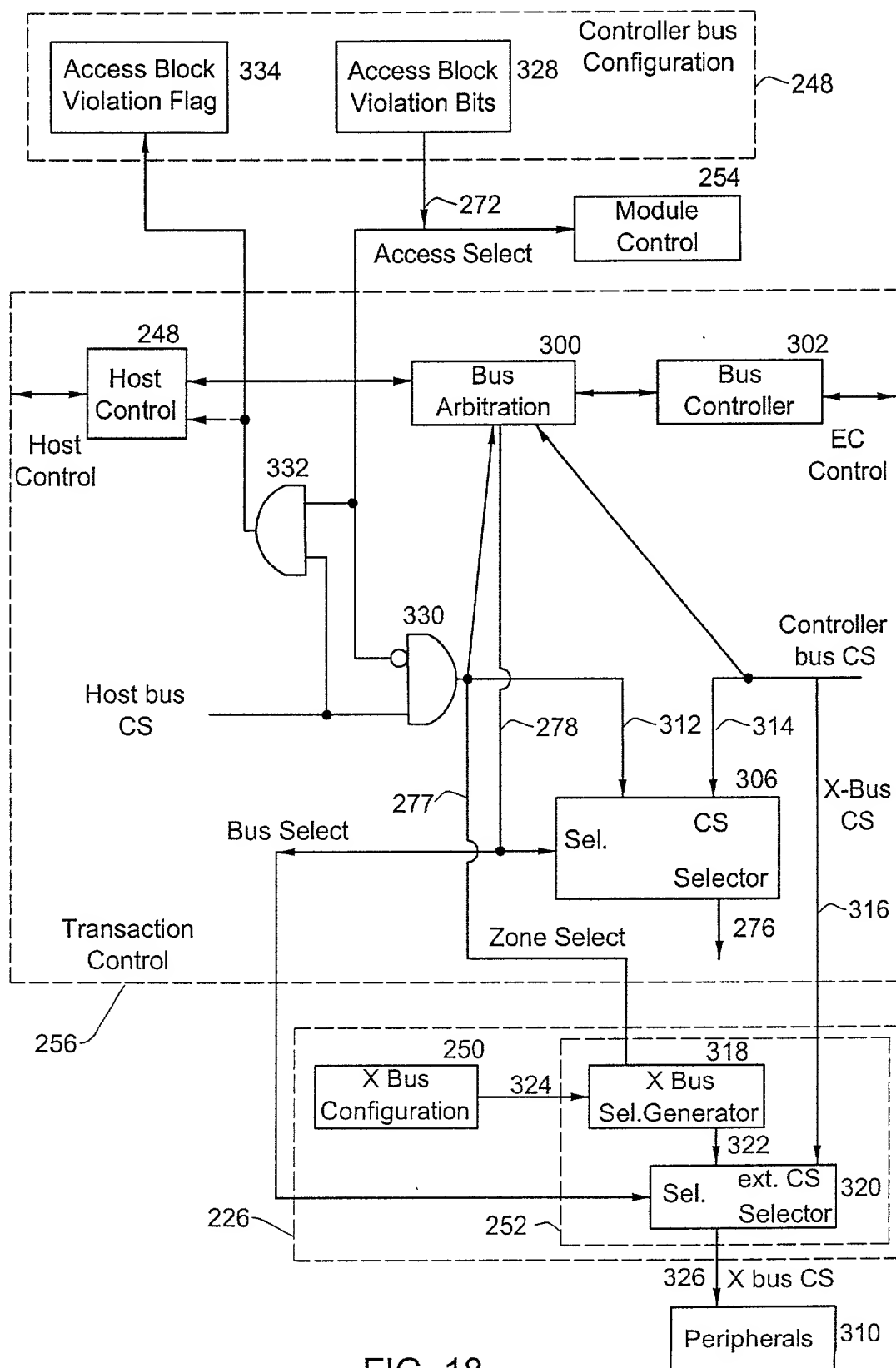


FIG. 18

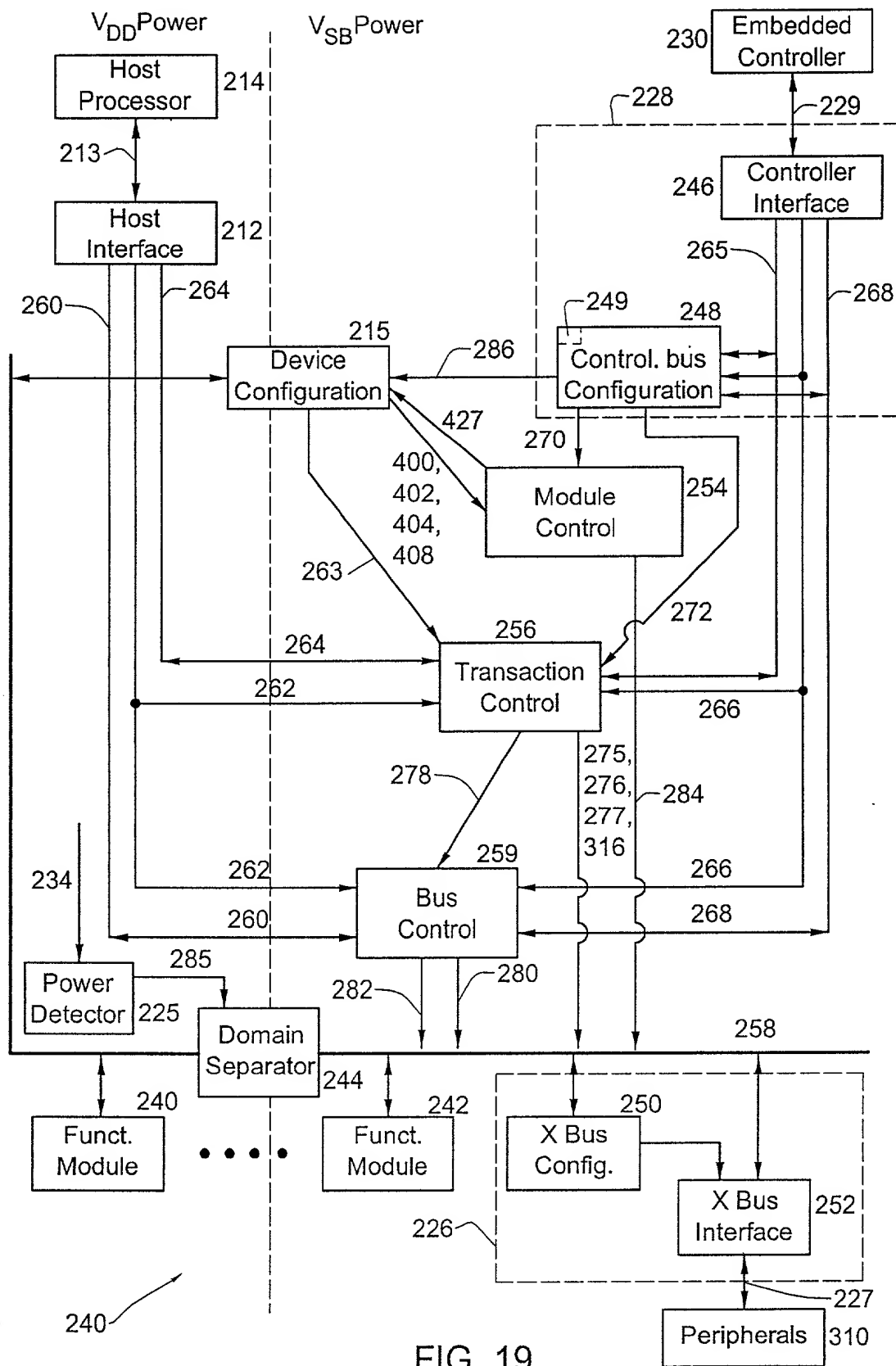


FIG. 19

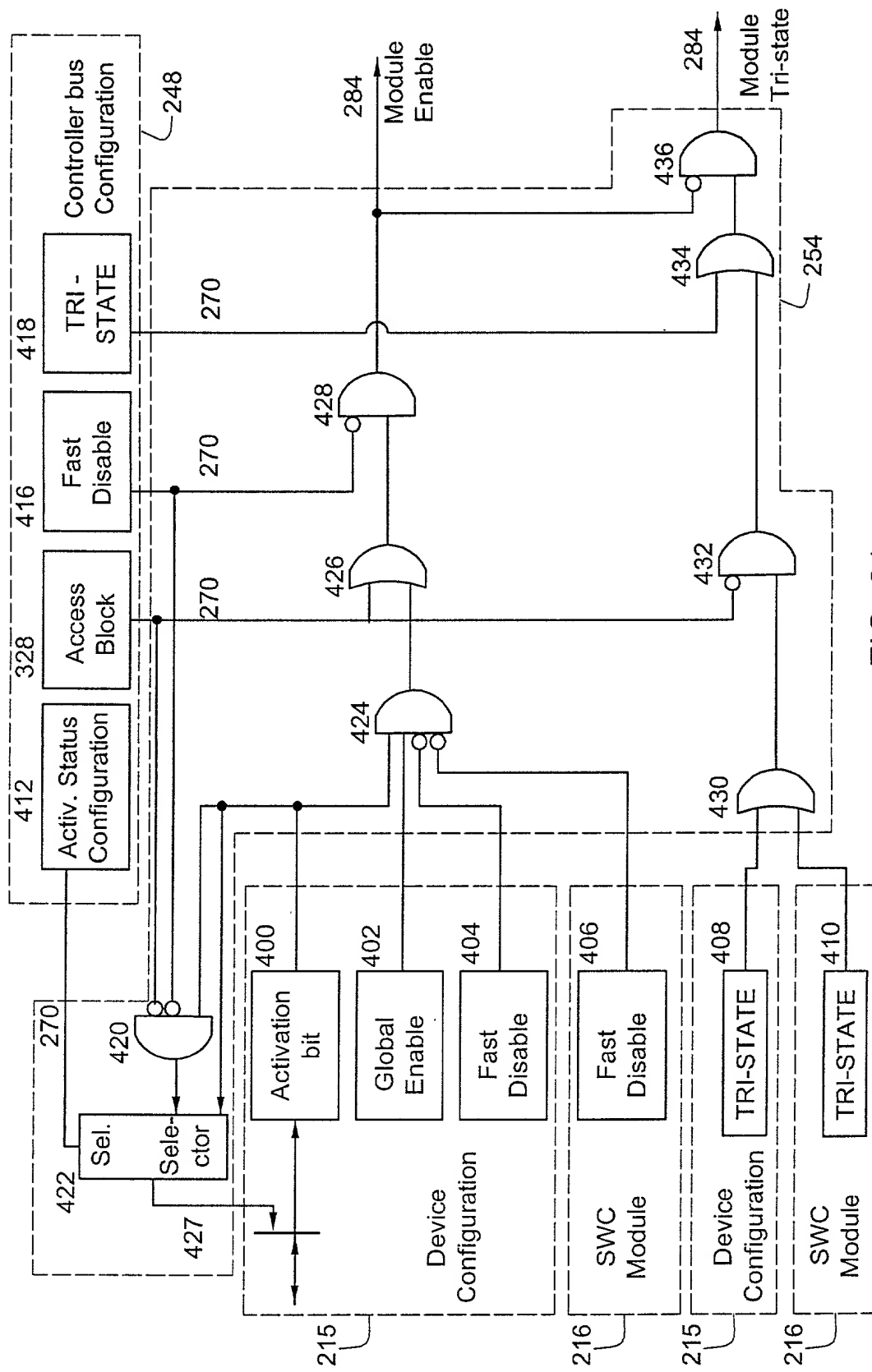


FIG. 21

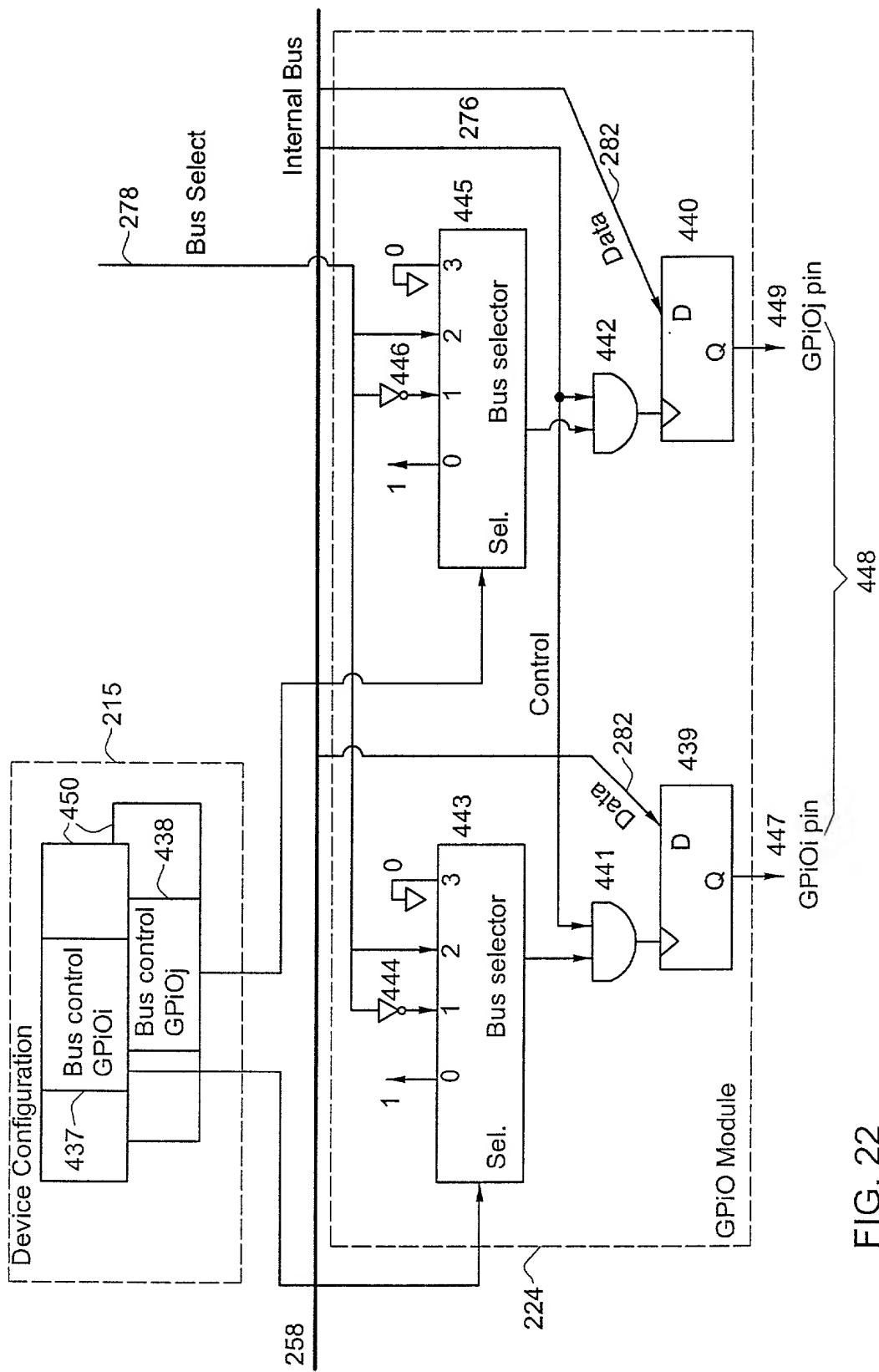


FIG. 22

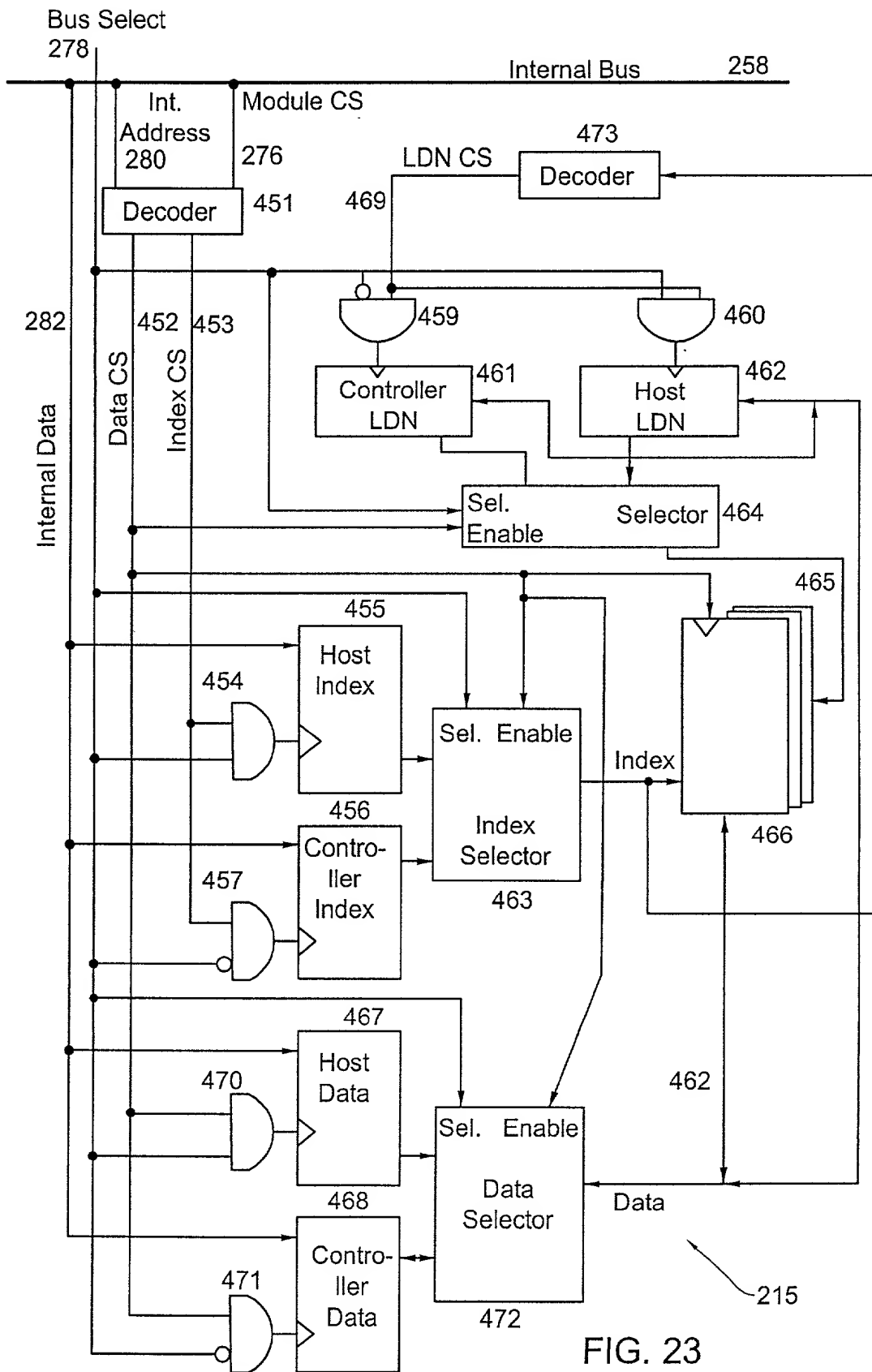


FIG. 23

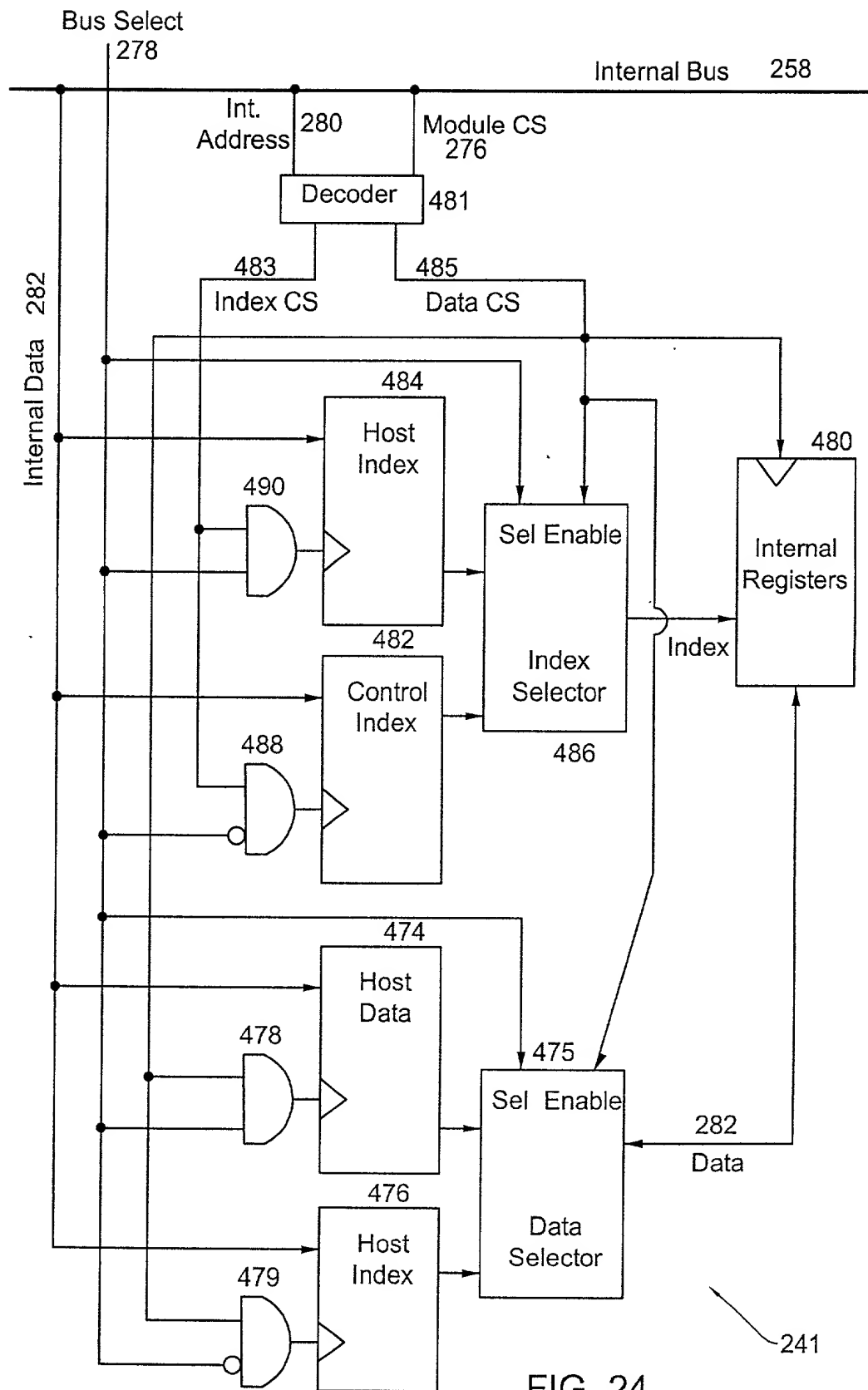


FIG. 24

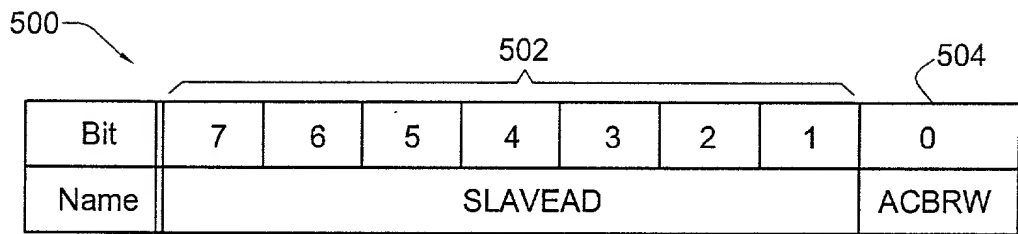


FIG. 25A

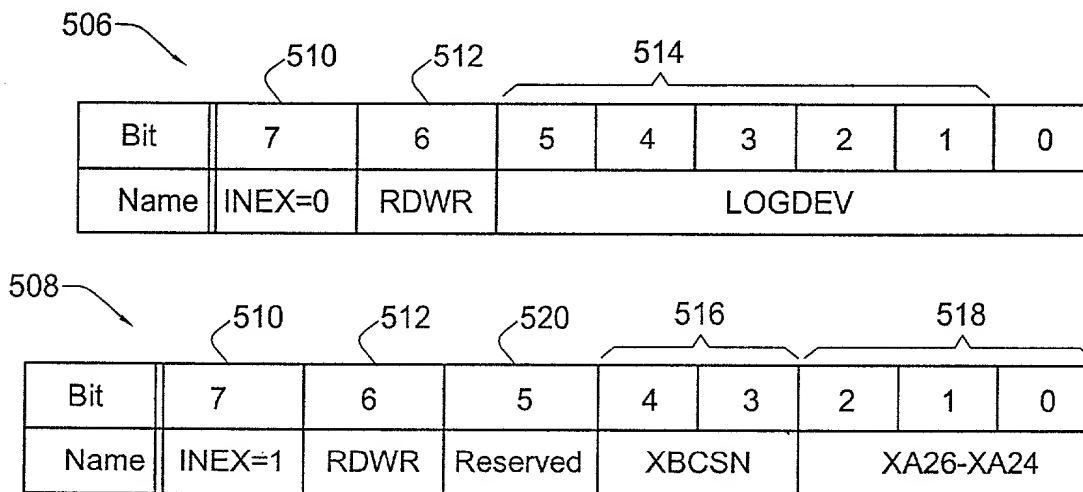


FIG. 25B

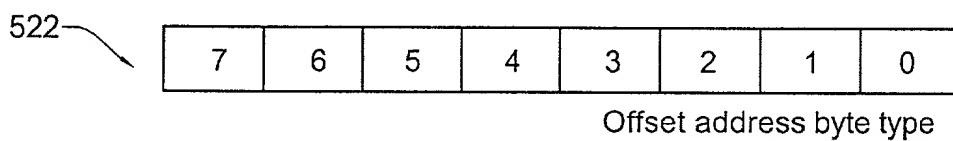


FIG. 25C

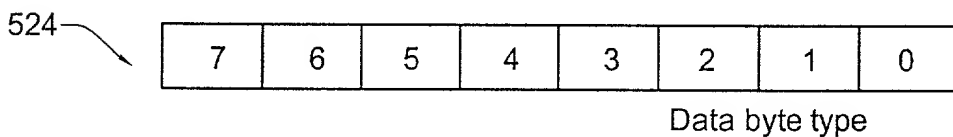


FIG. 25D

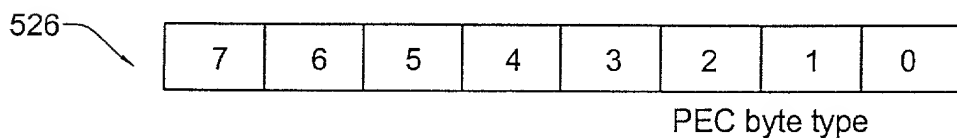


FIG. 25E

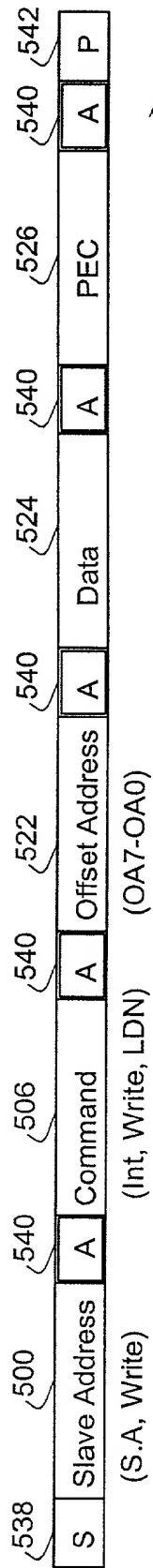


FIG. 26A

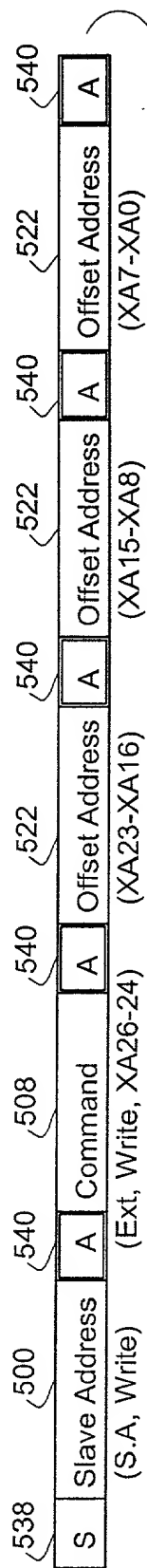


FIG. 26B

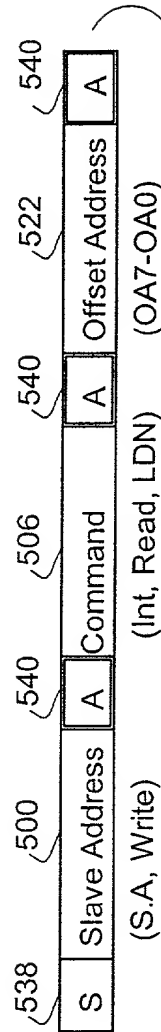
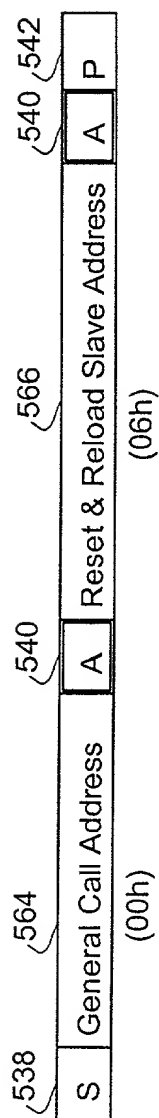
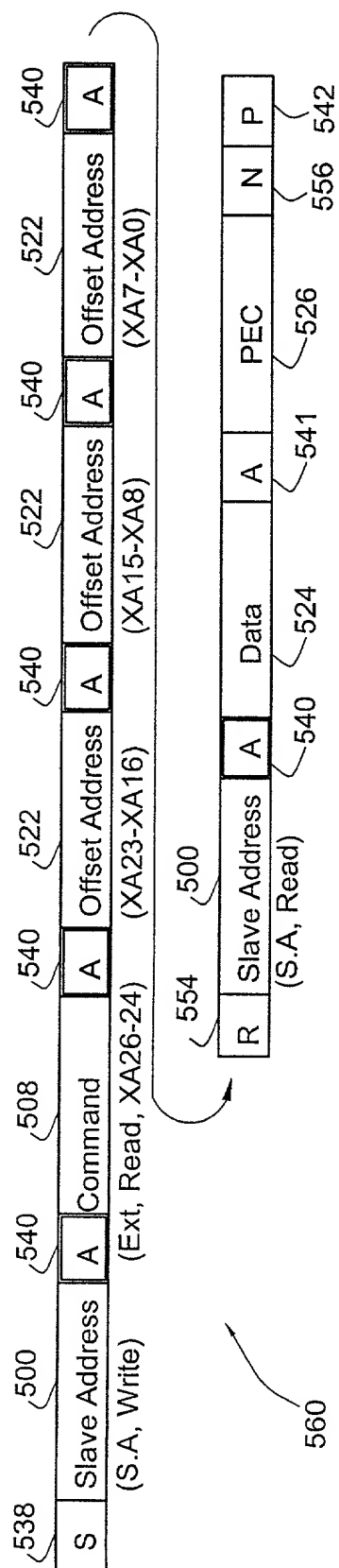
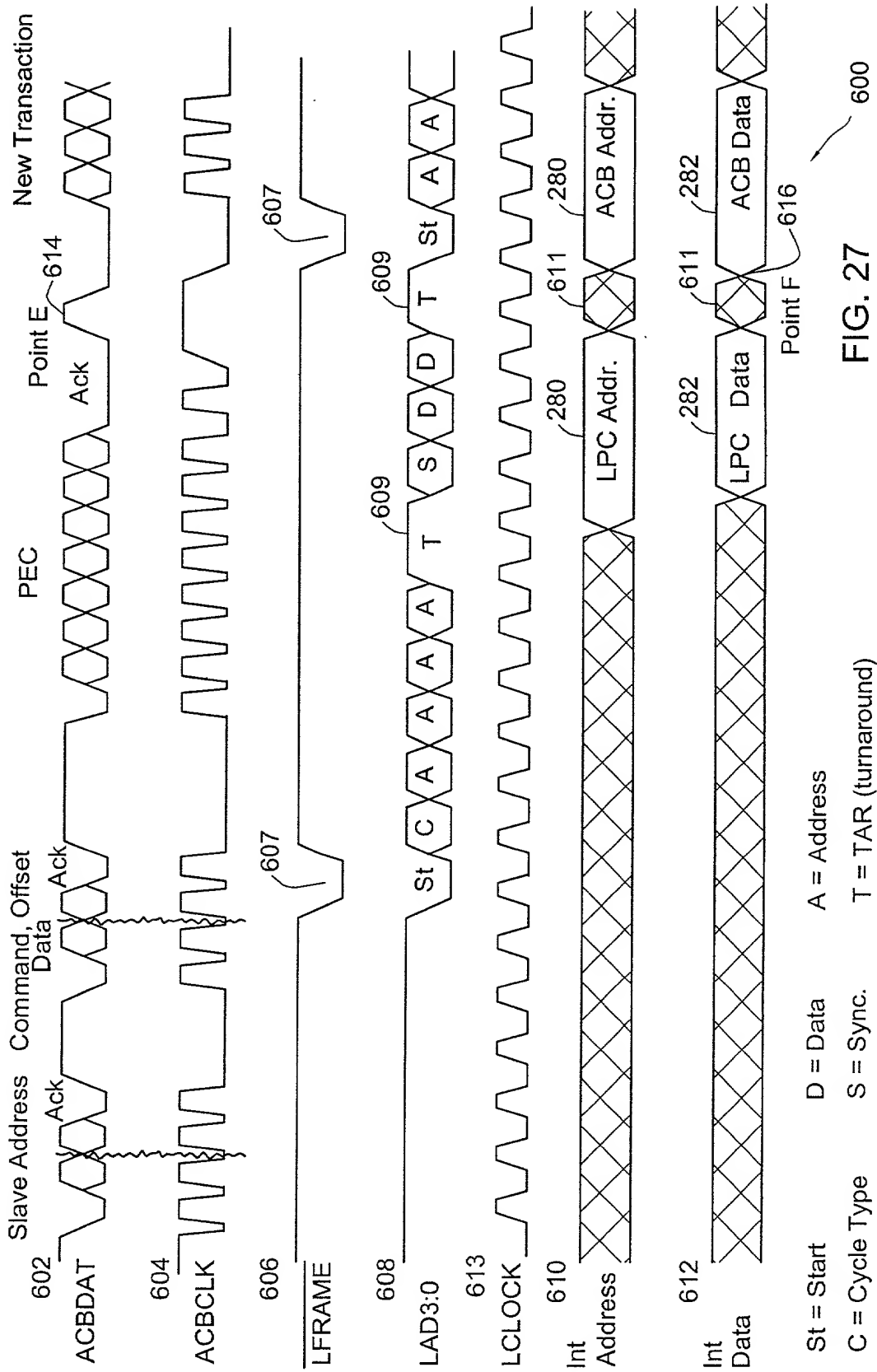


FIG. 26C

- S = Start condition
- P = Stop condition
- R = Restart cond.
- A = ACK by master
- N = NACK by master
- A = ACK by slave





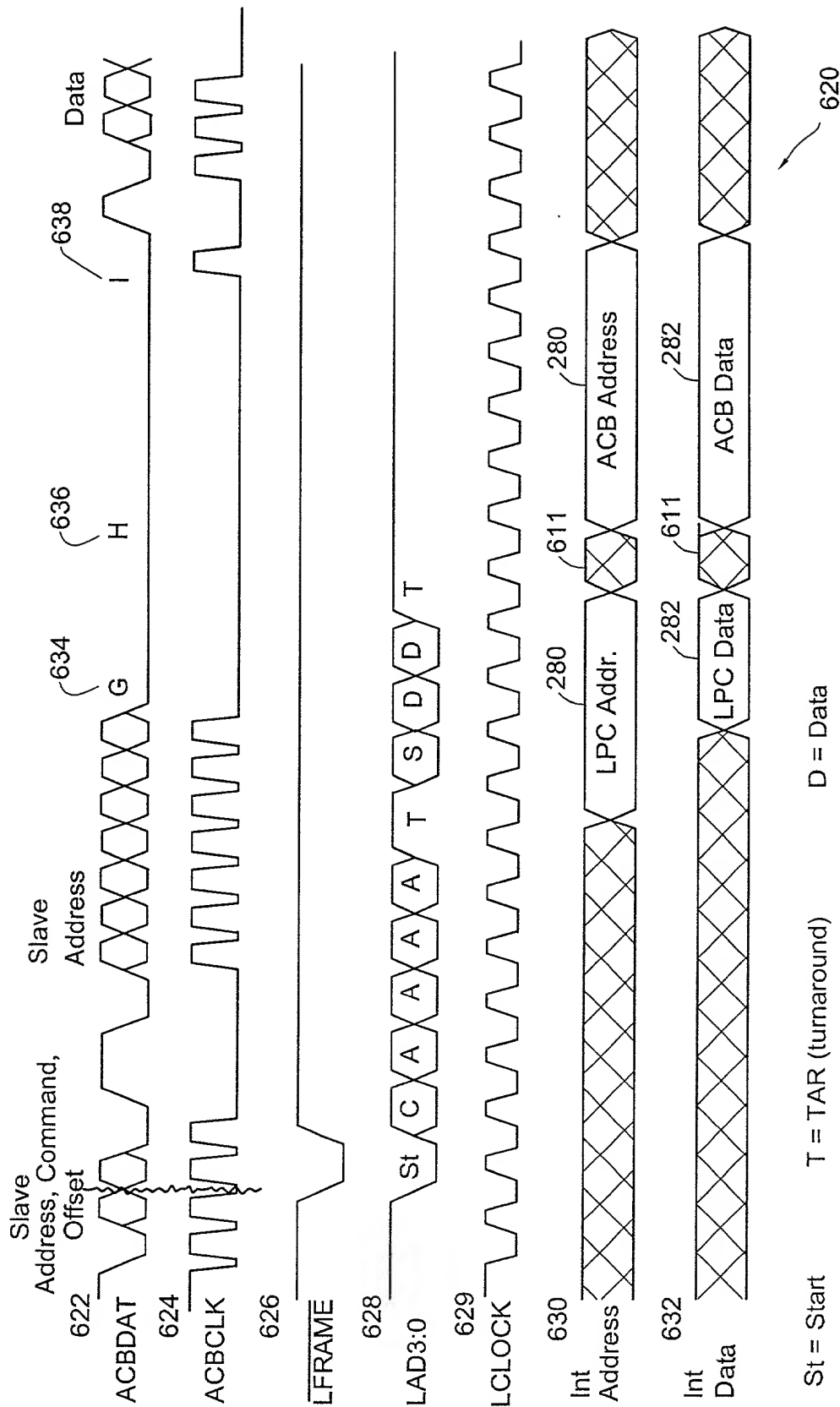


FIG. 28

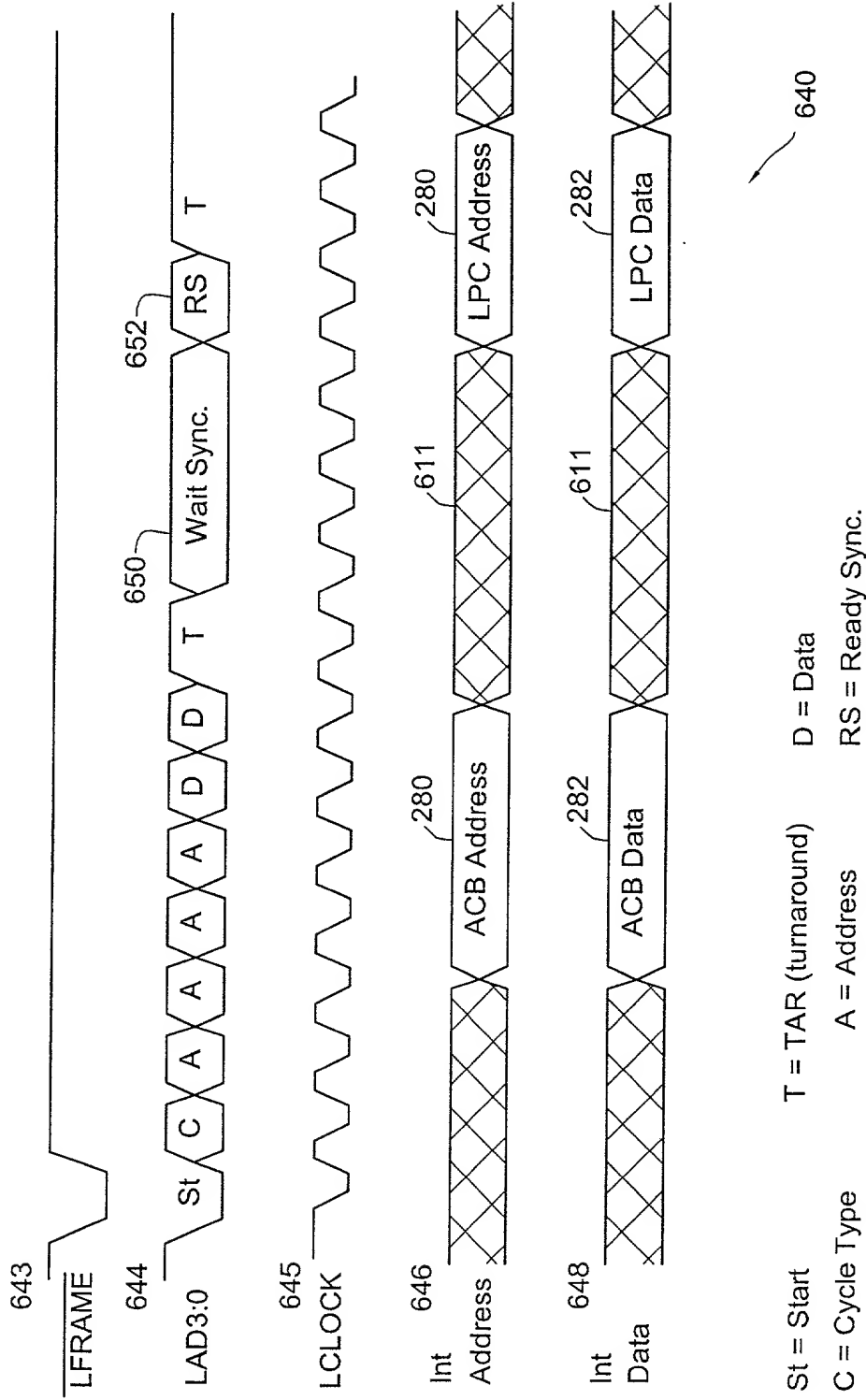


FIG. 29